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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



Applicant(s): Halahan, Patrick B.; Siniaguine, Oleg
Assignee: Tru-Si Technologies, Inc.
Title: Semiconductor Structures Having Multiple Conductive Layers In An Opening, And Methods For Fabricating Same
Application No.: 09/941,447 Filing Date: 08/28/2001
Examiner: Not yet known Group Art Unit: Not yet known
Docket No.: M-9999-1D US

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San Jose, California
June 10, 2002

COMMISSIONER FOR PATENTS
Washington, D.C. 20231

**INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR § 1.97(b)**

Dear Sir:

Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, the documents listed on the accompanying form PTO-1449 are called to the attention of the Examiner for the above patent application. Copies of these documents are not enclosed as all documents were previously cited by or submitted to the Patent Office in a prior application relied upon for priority under 35 U.S.C. 120 (37 C.F.R. § 1.98(d)). The prior application is U.S. Serial No. 09/792,311 filed February 22, 2001.

Citation of these documents shall not be construed as:

1. an admission that the documents are necessarily prior art with respect to the instant invention;
2. a representation that a search has been made; or
3. an admission that the information cited herein is, or is considered to be, material to patentability as defined in § 1.56(b).

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Respectfully submitted,

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